Translation

PATENT COOPERATION TREATY



PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference FP370PCT FOR FURTHER ACTION See Notification of Transmittal of Interpretation Preliminary Examination Report (Form PCT/IP)					
International application No.	International filing date (day/month/year) Priority date (day/month/year)				
PCT/JP2003/011894 18 September 2003 (18.09.2003)					
International Patent Classification (IPC) or national classification and IPC H02J 9/06, 9/00					
Applicant HITACHI, LTD.					
 This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36. This REPORT consists of a total of					
These annexes consist of a total of sheets.					
3. This report contains indications relating to the following items:					
I Basis of the report					
II Priority					
III Non-establishment of opinion with regard to novelty, inventive step and industrial applicability					
IV Lack of unity of in-	IV Lack of unity of invention				
Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
VI Certain documents cited					
VII Certain defects in the international application					
VIII Certain observations on the international application					
Date of submission of the demand	Date of completion of this report				
18 September 2003 (18.	09.2003) 17 February 2004 (17.02.2004)				
Name and mailing address of the IPEA/JP	Authorized officer				
Facsimile No.	Telephone No.				

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/JP2003/011894

I. Basis of	the report			
1. With reg	gard to the elements of the international application:*			
∑ th	e international application as originally filed			
th	e description:			
p:	ages, as originally filed			
p	ages, filed with the demand			
p:	ages, filed with the letter of			
Г	ne claims:			
	0000			
•	ages, as originally filed ages, as amended (together with any statement under Article 19			
	, a minima (cogonial with any statement under ruttere 17			
5	ages, filed with the demand ages, filed with the letter of,			
"	ne drawings:			
1	ages, as originally filed			
· ·	ages, filed with the demand			
l P	ages, filed with the letter of			
the	sequence listing part of the description:			
p	ages, as originally filed			
p	ages, filed with the demand			
p	ages, filed with the letter of,			
2. With regard to the language, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item. These elements were available or furnished to this Authority in the following language which is: the language of a translation furnished for the purposes of international search (under Rule 23.1(b)). the language of publication of the international application (under Rule 48.3(b)). the language of the translation furnished for the purposes of international preliminary examination (under Rule 55.2 and/ or 55.3). 3. With regard to any nucleotide and/or amino acid sequence disclosed in the international application, the international preliminary examination was carried out on the basis of the sequence listing: contained in the international application in written form. filed together with the international application in computer readable form. furnished subsequently to this Authority in written form. The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished. The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.				
5.	the description, pages the claims, Nos the drawings, sheets/fig the drawings as filed, as indicated in the Supplemental Box (Rule 70.2(c)).** **ement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to report as "originally filed" and are not annexed to this report since they do not contain amendments (Rule 70.16 1.17). **Delacement sheet containing such amendments must be referred to under item 1 and annexed to this report.			

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/JP03/11894

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
1. Statement					
Novelty (N)	Claims	1-8	YES		
	Claims		NO		
Inventive step (IS)	Claims		YES		
	Claims	1-8	NO		
Industrial applicability (IA)	Claims	1-8	YES		
	Claims		NO		

2. Citations and explanations

Document 1: JP 10-285832 A (Daikin Industries, Ltd.), 23 October, 1998

Document 2: JP 2001-28845 A (Asahi Kasei Microsystems Co., Ltd.), 30 January, 2001

Document 3: JP 2003-258113 A (Sanyo Denki Kabushiki Kaisha), 12 September, 2003

Document 4: JP 9-322429 A (Toshiba Corp.), 12 December, 1997

Document 5: JP 2638257 B2 (Yuasa Corp.), 25 April, 1997

Claims 1-5 and 7 do not appear to involve an inventive step based on document 1 (paragraphs [0003] – [0007], Fig. 3), document 2 (paragraph [00012], Fig. 3) and document 3 (paragraph [0015], Fig. 19) cited in the ISR.

Specifically, in the data backup device described in document 1 wherein a capacitor charged through a backflow prevention diode is set to be a reserve power source and when power source voltage reaches a prescribed voltage, mode switches to low-speed mode at which a microcomputer consumes little power, and when the voltage lowers further, the microcomputer is reset, using in place of a diode a MOS transistor connected to a diode, which is well-known, as can be seen by the descriptions in documents 2 and 3, and would be easy for a person skilled in the art.

Claim 6 does not appear to involve an inventive step based on documents 1-3 and document 4 (paragraph [0010]) cited in the ISR.

A digital circuit wherein a clock is stopped to shift to low power consumption is a conventional means, as can be seen from the descriptions in document 4.

Claim 8 does not appear to involve an inventive step based on documents 1-3 and 5 cited in the ISR.

As described in document 5, in document 1, resetting load after passage of a specific amount of time after power source voltage falls below a specific value would be easy for a person skilled in the art.